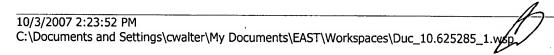
Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	24656167	@ad<"20030722"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L2	192	L1 and (general adj3 purpose adj3 event)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L3	52	L2 and bios	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L4	24656167	@ad<"20030722"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ .	ON .	2007/10/03 13:40
L5	553	L4 and (bios same (addres\$4 with map\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L6	95	L5 and ((address\$6 near15 decod\$5) same (ram or region or partition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L7	19	L5 and ((configur\$5 near15 manag\$5) same (address\$5 and BIOS))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ .	ON	2007/10/03 13:40

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L8	8	"379012" and (prgrammable or timing)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
. L9	2	L5 and 711/5.cor.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L10	0	L1 and ((address\$6 near15 decod\$5) same ((memory near20 (device module)) and (chip adj4 select)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L11		L1 and ((pin near6 (configur\$6 select)) with (chip or device))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L12	855	L1 and ((address\$6 near15 decod\$5) same ((memory near20 (device or module)) and (chip adj4 select)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ .	ON	2007/10/03 13:40
L13	5924	L1 and ((pin near6 (configur\$6 or select)) with (chip or device))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L14	99	L12 and ((address\$5 near10 decod\$5) same (significant adj4 bit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ .	ON	2007/10/03 13:40



L15	29	L13 and ((pin near6 (configur\$6 or select)) with ((boot\$4 or power\$1up) and ((chip or device) near8 select\$3)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L16	49	L1 and ((soft or warm) with (reset and config\$6 and register))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L17	0	((chip adj3 select) same ((encode\$7 and mode and default) and (boot\$5 near5 device))).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L18		L15 and (711/203 or 711/5 or 713/600).cor.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ .	ON	2007/10/03 13:40
L19	855	L1 and ((address\$6 near15 decod\$5) same ((memory near20 (device or module)) and (chip adj4 select)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L20	13	L19 and (711/203 or 711/5 or 713/600).cor.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L21	0	L1 and ((thumb\$1drive\$1 with switch) same (config\$5 and address))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40



L22	25	L1 and (non\$1volatile\$1 with	US-PGPUB;	ADJ	ON	2007/10/03 13:40
		(switch or dip)) same (config\$5 and address)	USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB			
L23	57	L1 and (non\$1volatile\$1 with (switch or dip)) same (config\$9 and address)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 13:40
L26	5	"711"/\$3.ccls. and (boot device).ab.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/10/03 14:23

